CBM003 ADD/CHANGE FORM

☐ Undergraduate Council  or  Graduate/Professional Studies Council
☐ New Course  ☑ Course Change
Core Category: NONE  Effective Fall 2010

1. Department: ECE  College: ENGR

2. Faculty Contact Person: John Glover  Telephone: X34430  Email: glover@uh.edu

3. Course Information on New/Revised course:
   - Instructional Area / Course Number / Long Course Title:
     ECE / 5367 / Introduction to Computer Architecture and Design
   - Instructional Area / Course Number / Short Course Title (30 characters max.)
     ECE / 5367 / INTRO TO COMPUTER ARCH & DSGN
   - SCH: 3.00  Level: SR  CIP Code: 14.0901.00.06  Lect Hrs: 3  Lab Hrs: 0

4. Justification for adding/changing course: **To provide flexibility in scheduling**

5. Was the proposed/revised course previously offered as a special topics course?  ☐ Yes  ☑ No
   If Yes, please complete:
   - Instructional Area / Course Number / Long Course Title:
     ____ / ____ / ____
   - Course ID: ____  Effective Date (currently active row): ____

6. Authorized Degree Program(s): BSEE Electrical Engineering, BSCpE Computer Engineering
   - Does this course affect major/minor requirements in the College/Department?  ☑ Yes  ☐ No
   - Does this course affect major/minor requirements in other Colleges/Departments?  ☐ Yes  ☑ No
   - Can the course be repeated for credit?  ☑ Yes  ☐ No (if yes, include in course description)

7. Grade Option: Letter (A, B, C …)  Instruction Type: lecture ONLY  (Note: Lect/Lab info. must match item 3, above.)

8. If this form involves a change to an existing course, please obtain the following information from
   the course inventory: Instructional Area / Course Number / Long Course Title
   ECE / 5367 / Introduction to Computer Architecture and Design
   - Course ID: 18912  Effective Date (currently active row): 20033

9. Proposed Catalog Description: (If there are no prerequisites, type in "none".)
   Cr: 3. (3-0). Prerequisites: ECE 3441 and credit for or concurrent enrollment in ECE 4436. Description
   (30 words max.): Computer organization, computer arithmetic, instruction sets, programming with MIPS
   assembly language, CPU design, pipelining, memory hierarchy including caching and virtual memory.

10. Dean’s Signature:  ___________________________ Date: 10/6/2009
   Print/Type Name: David P. Shattuck

- September 16, 2009 update -